

ADAPTIVE CACHE ALGORITHM  
FOR TEMPERATURE SENSITIVE MEMORY

Abstract of the Disclosure

A temperature sensitive memory, such as a ferro-electric polymer memory, may be utilized as a disk cache memory in one embodiment. If the temperature begins to  
5 threaten shutdown, the memory may be transitioned from a write-back to a write-through cache memory. In such case, the system is ready for shutdown without the loss of critical data.